

What is claimed is:

1. A semiconductor integrated circuit device for driving an address electrode of a display device according to display data,

wherein said semiconductor integrated circuit device includes a drive control part that includes:

an output part for outputting an electrode driving pulse for driving said address electrode of said display device according to a first change-over signal, a second change-over signal, and a driving pulse; and

an output driving part for driving said output part according to said display data,

wherein said output driving part outputs the driving pulse for driving said output part when said first data inputted first and said second data inputted after the input of said first data come to change both of said first and second data being included in said display data.

2. The semiconductor integrated circuit device according to claim 1,

wherein said output driving part includes:

a driving pulse generation part for generating a driving pulse from a latch signal;

a shift register for shifting inputted display data according to a shift pulse, then outputting said shifted display data;

a first latch for latching display data output from said shift register according to the latch signal;

a second latch for latching display data output from said first latch according to the driving pulse; and

a driving pulse output part for comparing said first data output from said first latch with said second data output from said second latch, then outputs the driving pulse to said output part when said first data does not match with said second data.

3. The semiconductor integrated circuit device according to claim 1,

wherein said output part includes:

an output circuit including a push-pull circuit in which first and second transistors are coupled serially between a first supply voltage and a reference potential;

a level shift circuit including a differential amplification circuit driven by said first supply voltage and the level shift circuit driving said first transistor that is a pull-up element of said output circuit according to said first change-over signal and said driving pulse; and

a driving part driven by a second supply voltage having a voltage value lower than that of said first supply voltage and the driving part driving said second transistor that is a pull-down element of said output circuit according to said second change-over signal.

4. A semiconductor integrated circuit device for driving an address electrode of a display device according to display data,

wherein said semiconductor integrated circuit device includes a driving control part that includes an output part for outputting an electrode driving pulse for driving said address electrode of said display device and an output driving part for driving said output part according to said display data, and

wherein said output driving part converts the output of said output part into a high impedance state according to a high impedance control signal when an output state of said output part is changed over.

5. The semiconductor integrated circuit device according to claim 4,

wherein said output driving part includes:

a shift register for shifting inputted display data according to a shift pulse, then outputting the shifted display data; and

a first latch for latching display data output from said shift register according to a latch signal; and

wherein said driving control part includes:

a signal generation part for generating first and second delay signals of which timings are different from each other according to latch signals;

a first driving pulse generation part for generating a first driving pulse according to the first delay signal output from said signal generation part;

a second driving pulse generation part for generating a second driving pulse according to the second delay signal output from said signal generation part;

a first selector for selecting either said first delay signal or second delay signal output from said signal generation part according to an output signal output from said first latch and the first selector for outputting the selected delay signal as a first or second change-over signal; and

a second selector for selecting one of said first and second driving pulses output from said first and second driving pulse generation parts according to an output signal output from said first latch and the second selector for outputting the selected driving pulse as a driving pulse.

6. The semiconductor integrated circuit device according to claim 4,

wherein said output driving part outputs the driving pulse for driving said output part when first data inputted first and second data inputted after the input of said first data come to change both of said first and second data being included in said display data.

7. The semiconductor integrated circuit device according to claims 4,

wherein said output part includes:

an output circuit that includes a push-pull circuit in which first and second transistors are coupled serially between a first supply voltage and a reference potential;

a level shift circuit that includes a differential amplification circuit driven by said first supply voltage and the level shift circuit driving said first transistor that is a pull-up element of said output circuit according to said first change-over signal and said first or second driving pulse selected by said second selector; and

a driving part driven by a second supply voltage having a voltage value lower than that of said first supply voltage and the driving part driving said second transistor that is a pull-down element of said output circuit according to said second change-over signal.